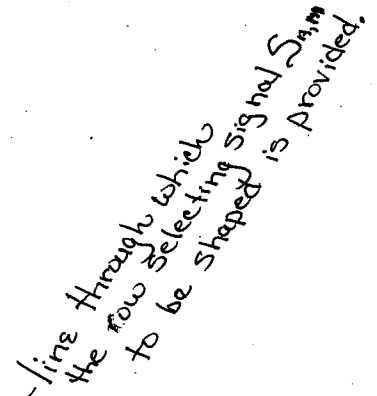


*line through which the associated gate signal is provided.*

**Fig.1**



Attached only for Examiner's Reference in  
Connection with Concurrently-Filed Remarks